

# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Bollman

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Applicant: BALAY et al.

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USPTO Unit: 2111

Date: November 28, 2006

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## U.S. PATENT DOCUMENTS

Examiner's Initials*	Document Number	Date MM/YYYY	Name (Family Name of First Inventor)	Class	SubClass	Filing Date (if appropriate)
	AR					
	BR					
	CR					
	DR					
	ER					
	FR					
	GR					
	HR					
	IR					
	JR					
	KR					
	LR					
	MR					
	NR					

## FOREIGN PATENT DOCUMENTS

	Document Number	Date MM/YYYY	Country	Inventor Name	Class	SubClass	English		Translation	
							Enclosed	No	Enclosed	No
	OR									
	PR									
	QR									
	RR									
	SR									
	TR									
	UR									
	VR									

## OTHER (Including in this order Author, Title, Periodical Name, Date, Pertinent Pages, etc.)

WR	LATTICE SEMICONDUCTOR CORPORATION, "ORCA@ ORT 4622 Field Programmable System Chip (EPSC) Four Channel x 622 Mbit/s Backplane Transceiver", Preliminary Data Sheet, October 2003pp. 1-78									
XR										
YR										
ZR										
AAR										
BBR										

Examiner

Date Considered: 2/2/07

\*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.